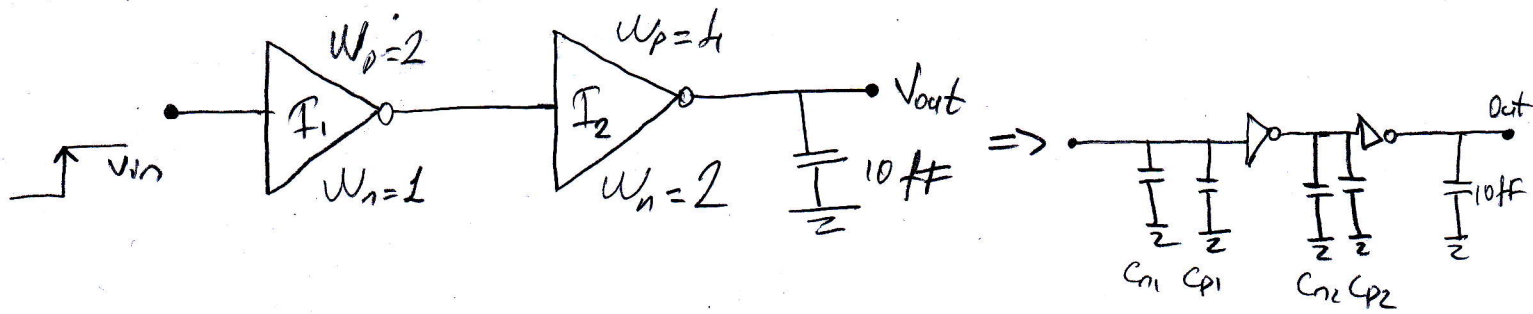


Quitz-1, 2)



→ Since input is low-to-high, I_1 output is high to low.

→ Since input I_2 is output I_1 , I_2 output is low-to-high.

⇒ Calculating both delays according to their characters;

$$t_{PHL_{I_1}} = (C_L / C_N) \tau_n = \left(\frac{C_{P2} + C_{N2}}{C_{N2}} \right) \tau_n = \left(\frac{C_{ox} (4 \cdot 1 \cdot 10^{-12} + 2 \cdot 1 \cdot 10^{-12})}{C_{ox} (1 \cdot 1 \cdot 10^{-12})} \right) \tau_n = 6 \cdot \tau_n = \boxed{6 \text{ ps}}$$

$$t_{PHL_{I_2}} = (C_L / C_P) \tau_p = \left(\frac{10 \cdot 10^{-15}}{C_{ox} (4 \cdot 1 \cdot 10^{-12})} \right) = \left(\frac{10}{4} \right) \tau_p = 2.5 \tau_p = \boxed{2.5 \text{ ps}}$$

→ total delay = $6 + 2.5 = \boxed{8.5 \text{ ps}}$

1) All statements are FALSE

a) Increase in $W_n \rightarrow$ decreasing $R_n \rightarrow$ increased leakage currents \rightarrow V_{OH} can't increase.

b) $t_{PHL} = s_p \tau_p \rightarrow$ W_n has no effect in this equation.

c) $V_I = \frac{V_{DD} - |V_{TP}| + \sqrt{\frac{B_n}{B_p}} V_{Tn}}{1 + \sqrt{\frac{B_n}{B_p}}} \rightarrow$ k_n', k_p' could differ. There is no certainty.

d) $C_{out} = \underbrace{[(C_{GDn} + C_{GDP}) + (C_{DBn} + C_{DBP})]}_{C_{int}} + C_L$
 $C_{int} \rightarrow$ no C_{GS} capacitance considered